

(19) World Intellectual Property Organization  
International Bureau(43) International Publication Date  
17 May 2001 (17.05.2001)

PCT

(10) International Publication Number  
WO 01/35649 A1(51) International Patent Classification<sup>7</sup>: H04N 5/783

(21) International Application Number: PCT/US00/30344

(22) International Filing Date:  
3 November 2000 (03.11.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
60/164,793 10 November 1999 (10.11.1999) US

(71) Applicant (for all designated States except US): THOMSON LICENSING S.A. [FR/FR]; 46, Quai Alphonse Le Gallo, F-92648 Boulogne Cedex (FR).

(72) Inventors; and

(75) Inventors/Applicants (for US only): XIE, Jianlei, James [CN/US]; 11852 Stoney Bay Circle, Carmel, IN 46033 (US). SCHULTZ, Mark, Alan [US/US]; 4437 Somerset Way S., Carmel, IN 46033 (US). RICH, Michael, Dillion [US/US]; 9653 Aberdeen Court, Fishers, IN 46028 (US).

(74) Agents: TRIPOLI, Joseph, S. et al.; Thomson Multimedia Licensing Inc., P.O. Box 5312, Princeton, NJ 08540 (US).

(81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.

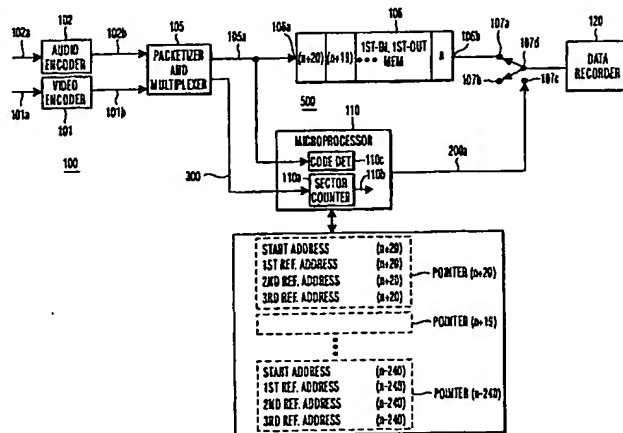
(84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

## Published:

- With international search report.
- Before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: AN ARRANGEMENT FOR RECORDING NAVIGATION INFORMATION IN A DATA RECORDER



(57) Abstract: A data recorder for a digital video disc includes a first-in, first-out buffer memory (106). The memory (106) is responsive to an input data signal for sequentially storing in a first-in, first-out manner a running, data of Video Object Units (VOBU'S), defined in the Digital Versatile Disc (DVD) Specification for Read Only Disc (the specification), prior to recording the data. In a first embodiment of the invention, the value of each address pointer of a first sub-set of address pointers contained in a field VOB Search Information (VOBU SRI), are established in accordance with the VOBUs contained in the buffer memory (106). The value of each address pointer of a second sub-set of the address pointers, associated with VOBUs positioned further behind, in a sequence of presentation, than each of the VOBUs contained in the memory (106) is not made to conform to the Specification. Thereby, a storage capacity requirement from the buffer memory (106) is reduced. In a second embodiment of the invention, the size of each Cell in the data format is constrained to only 59 VOBUs. The entries for each address pointer of the second sub-set, is made to contain a "does not exist" code.

## AN ARRANGEMENT FOR RECORDING NAVIGATION INFORMATION IN A DATA RECORDER

### BACKGROUND OF THE INVENTION

5 In a Digital Video Disc (DVD) for read-only disk type application, packetized video data, conforming to the Motion Picture Expert Group #2 (MPEG-2) standard for compression, and/or audio data are multiplexed onto a DVD disc. The data are recorded in a manner compliant with Part 3, VIDEO SPECIFICATIONS of the existing DVD Specifications for Read-Only Disc (the  
10 Specification). The read-only DVD data format employs a sub-set of the MPEG-2 format and is defined by a proprietary standard adopted by a consortium of consumer electronics companies and described in available literature such as "DVD Demystified-The Guidebook of DVD-Video and DVD-ROM", 1997, by Jim Taylor published by McGraw Hill. Further, MPEG-2 compatible data is encoded in  
15 accordance with the "MPEG standard" comprised of a system encoding section (ISO/IEC 13818-1, 10th June 1994) and a video encoding section (ISO/IEC 13818-2, 20th January 1995).

Multiplexed data are segmented into logical blocks referred to as Video Object Units (VOBU's) forming a presentation sequence. FIGURE 1 illustrates  
20 schematically a normal forward presentation sequence that includes, for example, VOBUs (n-m), VOBUs (n) and VOBUs (n+m) in that order. The number represented by the symbol "n" provides the position of the VOBUs within the sequence. Current VOBUs (n) represents, for example, the VOBUs that currently provides the video and audio information for playback or for recording, in  
25 accordance with the context. Similarly, future VOBUs (n+m) provides the video and audio information following current VOBUs (n) and is separated from current VOBUs (n) by a number of intervening VOBUs determined by the numerical value of the symbol "m". In the same manner, past VOBUs (n-m) provides the video and audio information prior to current VOBUs (n).

30 FIGURE 2 illustrates schematically the data structure within VOBUs (n). Similar symbols and numerals in FIGURES 1 and 2 indicate similar items or functions. The video information of VOBUs (n) of FIGURE 2 is encoded in

accordance with the MPEG-2 standard and recorded in data fields, each occupying a single sector on the disc, for example, a data field or portion Video Pack (V-PCK) 202. Similarly, audio information is recorded in data fields, each occupying a single sector on the disc, for example, a data field or portion Audio Pack (A-PCK) 203.

Additionally, the data structure of VOB (n) includes a single data field or portion Navigation Pack (Nav\_Pack) 201, containing information concerning the presentation control of VOB (n) and occupying a single sector on the disc. Data field Nav\_Pack 201 includes a data field referred to in the specification as Data Search Information (DSI), not shown, that includes a data field VOB Search Information (VOB\_SRI) 201a having a length of 168 bytes. Field VOB\_SRI 201a contains information selectively used for searching data in either future or past VOB's relative to current VOB (n) in order to perform "trick" play operation such as fast reverse or fast forward. In the trick play operation, a subset of the pictures VOB's in the presentation sequence provide content information for achieving a look that is faster than in the normal presentation sequence.

The table of FIGURE 3 includes FIGURES 3A and 3B. A given 4-byte address pointer or data word FWDI(m), shown in the table of FIGURE 3 and contained in field VOB\_SRI 201a of current VOB (n) of FIGURE 1, identifies a start, logical sector address of a corresponding future VOB, not shown, associated with data word FWDI(m). Similar symbols and numerals in Figures 1, 2 and 3 indicate similar times or functions. In normal forward presentation mode, a future VOB such as, for example, future VOB (n + m), associated with data word FWDI(m) is presented for display, following an interval measured from the presentation start time of current VOB (n) that has a duration, in seconds, equal to 0.5 multiplied by the value of "m". The parameter "m" is selected from the consecutive integer values, 1 through 15 and the integer values 20, 60, 120 and 240. For example, word FWDI(m = 20) refers to a future VOB which is presented in the normal forward presentation sequence 20 VOB's following current VOB (n).

Similarly, each 4-byte address pointer or data word BWDI(m) in the table of FIGURE 3 identifies the start, logical sector address of a corresponding past VOB, for example, VOB (n-m), associated with data word BWDI (m). The past VOB associated with data word BWDI (m) is presented for display, in normal forward presentation mode, before the presentation start time of current VOB (n) by an interval having a duration, in seconds, equal to 0.5 multiplied by the value of "m". The parameter "m" can be selected from the same integer values. For example, word BWDI(m = 20) refers to a VOB which appears is in the presentation sequence 20 VOB's before current VOB (n).

In a trick mode operation, a DVD player, not shown, conforming to the Specification, may obtain the address of the next VOB for processing from the corresponding entry, word FWDI(m) or BWDI(m), in field VOB\_SRI 201a of current VOB (n) of FIGURE 2 . Thereby, the presentation of the video information contained in intervening VOB's is omitted or skipped over to provide for fast forward or fast reverse trick mode operations. For example, when, in the fast trick mode the 21<sup>th</sup> future VOB is presented immediately after current VOB (n), the presentation of the 20 VOB's, interposed between them, are skipped over. When displaying 1-only pictures for 10 fields per 1 picture, the result is a fast trick mode operation that is approximately 60 times faster than in the normal presentation mode.

It may be desirable to perform a real-time recording of audio and video content on discs compatible for playback operation with existing read-only DVD players, intended for discs conforming to the Specification. That means that data words FWDI(m) and BWDI(m) have to be provided in the data stream. Recording data words BWDI(m) in current VOB (n) does not necessitate the usage of a large buffer memory in the recorder. This is so because the sector addresses of past VOB's associated with data words BWDI(m) could be computed prior to receiving the data of current VOB (n).

On the other hand, accumulating appropriate values for recording data word FWDI (m) might, disadvantageously, require a large buffer memory. Because of a variable bit-rate in MPEG encoding, without extensive data buffering, it cannot be known at the time the video data, for example, of current

VOBU (n) is produced by the MPEG encoder what addresses the future VOBUs will have.

In order to record in field VOBUSRI 201a, for example, word FWDI(m = 240) of FIGURE 3, related to the 241<sup>th</sup> future VOBUs, all the video information of the intervening VOBUs has to be encoded, assembled and stored for computing the starting sector address of the 241<sup>th</sup> future VOBUs. The 241<sup>th</sup> future VOBUs appears 240 VOBUs after current VOBUs (n). Therefore, the information associated with each of the intervening 240 future VOBUs might have to be stored in a memory prior to recording on the disc the information of field VOBUSRI 201a of current VOBUs (n). After each of the 241 future VOBUs has been produced, words FWDI(m) of the table of FIGURE 3 could be generated. Disadvantageously, storing such a large number of VOBUs requires an excessively large storage space in the memory in the range of 100 Mega-Byte.

In carrying out an inventive feature, the address pointer for each of the 61<sup>th</sup>, 121<sup>th</sup> and 241<sup>th</sup> future VOBUs, ahead of current VOBUs (n) of FIGURE 1, that, according to the Specification, should be contained in words FWDI(m = 60), FWDI(m = 120) and FWDI(m = 240), respectively, instead, is set to a value that need not conform to the Specification. Whereas, the address contained in each of words FWDI(m = 1) through FWDI(m = 20) is made to conform to the Specification. Consequently, the maximum fast forward trick mode, that corresponds to word FWDI(m = 20), is 60 times faster than in the normal presentation mode, as indicated before. Advantageously, such an arrangement reduces the memory requirement significantly, to the order of 8 Mega-Byte.

In carrying out a further inventive feature, in each of words FWDI(m = 60), FWDI(m = 120) and FWDI(m = 240), respectively, the value of, for example, word FWDI(m = 20) associated with the 20<sup>th</sup> future VOBUs is, instead, recorded. Thereby, a DVD player having the capability of operating in a fast forward trick mode, faster than 60 times the normal presentation mode, will be limited to the 60 times faster trick mode.

The logical sector addresses provided in field VOBUSRI 201a are valid, in accordance with the Specification, for a predetermined group of VOBUs referred

to as a "Cell" such as Cell 206 of FIGURE 1. Cell 206, for example, is a logical association of VOBUs that includes, for example, VOBUs (n-m), VOBUs (n) and VOBUs (n + m), and the intervening VOBUs. Cell 206 is delineated by two sector addresses containing a start logical sector number and an end logical sector number, respectively. In the Specification, Section 4.5.4, subsections 2 and 4, it is stated that if a given word FWDI (or BWDI) were to point to an address of a VOBUs outside the range of a Cell, such as Cell 206, then, instead, it should contain a bit combination referred to as "does not exist" code, rather than an address pointer of an actual VOBUs.

In accordance with another inventive feature, each Cells is constrained in size to only 59 VOBUs. As a result of constraining each Cell in size to only 59 VOBUs, the entries for each of words FWDI(m = 60), FWDI(m = 120) and FWDI(m = 240) need only contain the aforementioned "does not exist" code. Therefore, advantageously, the need for a large buffer memory is diminished.

#### SUMMARY OF THE INVENTION

A data recorder apparatus, embodying an aspect of the invention, is used for recording in a recording medium navigation data, facilitating a trick, playback mode of operation, and information data contained in each data unit of a sequence of data units forming an input data signal. The data recorder apparatus includes a buffer memory responsive to the input data signal for storing the information data of a running plurality of the data units and for reading out the stored information data of the running plurality of data units, sequentially. The information data of a data unit next-in-line to be recorded are developed at an output of said buffer memory. A value conforming to a position of a first address pointer of a plurality of address pointers of the navigation data of the next-in-line data unit is produced when the information data of a data unit associated with the first address pointer position and referred to by the first address pointer value are present concurrently with the information data of the next-in-line data unit in the buffer memory. The conforming value identifies where in the recording medium the data information of the associated data unit are designated to be recorded. The production of a value conforming to a

position of a second address pointer of the plurality of address pointers is avoided, when the data unit associated with the second address pointer and the next-in-line data unit are included in different cells. A number of data units that are included in the common cell are limited to a limit value determined in accordance with a maximum number of data units having information data that can be contained concurrently in the buffer memory. The information data and the navigation data of the next-in-line data unit are recorded in the recording medium.

A data recorder apparatus, embodying another aspect of the invention, is used for recording in a recording medium navigation data, facilitating a trick, playback mode of operation, and information data contained in each data unit of a sequence of data units partitioned into cells and forming an input data signal. The data recorder apparatus includes a buffer memory responsive to the input data signal for storing the information data of a running plurality of the data units and for reading out the stored information data of the running plurality of data units, sequentially. The information data of a data unit next-in-line to be recorded are developed at an output of the buffer memory. A value conforming to a position of a first address pointer of a plurality of address pointers of the navigation data of the next-in-line data unit is produced when a data unit associated with the first address pointer position is included with the next-in-line data unit in a common cell. The conforming value identifies where in the recording medium the data information of the associated data unit are to be recorded. The production of a value conforming to a position of a second address pointer of the plurality of address pointers is avoided when a data unit associated the second address pointer and the next-in-line data unit are included in different cells. A number of data units that are included in the common cell are limited to a limit value determined in accordance with a maximum number of data units having information data that can be contained concurrently in the buffer memory. The information data and the navigation data of the next-in-line data unit are recorded in the recording medium.

BRIEF DESCRIPTION OF THE DRAWINGS

In the drawing:

FIGURE 1 illustrates schematically data structure of a Cell, in accordance with the prior art Specification;

5       FIGURE 2 illustrates schematically structure of data a Video Object Unit (VOBU) of the Cell of FIGURE 1, in accordance with the prior art Specification;

FIGURE 3, formed by Figures 3A and 3B, shows in a table form the data structure within a portion of a Navigation Pack of the Video Object Unit of FIGURE 2, in accordance with the prior art Specification; and

10       FIGURE 4 illustrates in a block diagram a data processing arrangement, in accordance with an aspect of the invention, for a data recorder.

DETAILED DESCRIPTION

FIGURE 4 illustrates in a block diagram a data processing arrangement 15 500, in accordance with an aspect of the invention, for a data recorder 120. An MPEG encoder 100 includes a video data encoder 101. A data input signal 101a, obtained from, for example, a National Television Standard Committee an (NTSC) decoder, not shown, is coupled to video data encoder 101 for encoding video information in accordance with the MPEG-2 standard in a conventional manner to 20 produce a video data signal 101b. A conventional audio AC-3 (Audio compression version 3) encoder 102, responsive to a stereo audio input signal 102a, is used for generating an AC-3 data signal 102b containing audio information. Signals 102b, and 101b are multiplexed and packetized in a packetizer/multiplexer 105 to form a multiplexed data signal 105a.

25       The data of data signal 105a are logically partitioned to sequentially produced Cells, in accordance with the Specification, such as, for example, Cell 206 of FIGURE 2. Each Cell contains VOBUs having a data structure substantially the same as of VOBUs (n), except for differences noted below. In signal 105a, the bit count or data space of, for example, a data field Nav\_Pack 30 201' of VOBUs (n), that is analogous to data field Nav\_Pack 201, is the same as in data field Nav\_Pack 201. However, a portion of data field Nav\_Pack 201' in



signal 105a contains "dummy" data that are subsequently modified, as described later on.

Data signal 105a is also coupled to a first-in, first-out buffer memory 106 for storing the data of each most recently appearing VOB, VOB (n + 20), contained in signal 105a. When the immediately following VOB appears in  
5 signal 105a, it is referred to as VOB (n + 20) and the previously appearing VOB (n + 20) is re-designated VOB (n + 19), and so on.

Memory 106 has a capacity to contain the running data of the most recently produced VOB's, VOB (n + 20) through VOB (n). Thus, the data of  
10 VOB (n + 20) are the last to be stored in memory 106. The data of VOB (n) appear in an output signal 106a of memory 106.

The portion of signal 106a that does not contain "dummy" data but the real data to be recorded is coupled to a data recorder 120 for recording in a disc, not shown, of data recorder 120 via a first input terminal 107a. Input terminal  
15 107a is coupled to an output terminal 107d of a switch 107. The coupling between terminals 107a and 107d is controlled by microprocessor 110 via a control terminal 107c. After being recorded, the data of VOB (n) is eliminated from memory 106 in a first-in, first-out manner.

Encoder 100 generates a flag signal 300 each time the data of a new  
20 sector appears in signal 105a. Signal 300 is coupled to microprocessor 110 for incrementing a sector counter 110a each time signal is generated. Each sector data of signal 105a includes a header portion, not shown, identifying the type of sector data such as, for example, identifying the data as being of the same type as portion V-PCK 202, A-PCK 203 or Nav\_Pack 201.

When the header portion, not shown, of data field Nav\_Pack 201', is  
25 detected in a code detector 100c, indicating that the data for a new VOB (n + 20) are about to follow, microprocessor 110 stores, in a first-in, first-out navigation information memory 200, a word START-ADDRESS (n + 20). Word START-ADDRESS(n + 20) contains the present value of an output signal 110b of  
30 sector counter 110a. Thus, word START - ADDRESS (n + 20) contains the beginning sector address of VOB (n + 20).

Immediately following the detection in signal 105a of the header portion, microprocessor 110 begins searching in the data stream of signal 105a for the occurrence of a 32-bit Picture-Start-Code, not shown, defined in the Specification. After detecting the Picture-Start-Code in signal 105a, indicating  
5 the beginning sector of a first reference, I-type picture frame, not shown, in accordance with MPEG, microprocessor 110 continues searching for the occurrence of an immediately following Picture-Start-Code in the data stream of signal 105a. When the immediately following Picture-Start-Code is detected, microprocessor 110 stores in navigation information memory 200 a word FIRST-  
10 REFERENCE-ADDRESS ( $n + 20$ ) containing the then present value of sector counter 110a. Word FIRST-REFERENCE-ADDRESS ( $n + 20$ ) contains the last sector number or address of the first reference, I-type picture frame, defined in the Specification, in VOB ( $n + 20$ ). The last sector number is also the first sector number of the next picture frame.

15 In a similar manner, microprocessor 110 stores in navigation information memory 200 a word SECOND-REFERENCE-ADDRESS ( $n + 20$ ), containing a sector number or address of the last sector of a second reference, I or P-type picture frame, not shown, in VOB ( $n + 20$ ). Similarly, a word  
THIRD-REFERENCE-ADDRESS ( $n + 20$ ) is stored, containing a sector number of  
20 the last sector of a third reference, I or P-type picture frame, in VOB ( $n + 20$ ).

The group of stored words START-ADDRESS( $n + 20$ ), FIRST-REFERENCE-ADDRESS( $n + 20$ ), SECOND-REFERENCE-ADDRESS( $n + 20$ ) and  
THIRD-REFERENCE-ADDRESS( $n + 20$ ), referred to herein, collectively, as a word  
group POINTER ( $n + 20$ ), is associated with VOB ( $n + 20$ ). When the  
25 immediately following VOB appears in signal 105a, its corresponding information will be stored in memory 200 that will be referred to as word group  
POINTER ( $n + 20$ ). Whereas, the previously stored word group POINTER ( $n + 20$ )  
will be redesignated as word group POINTER ( $n + 19$ ), and so on. Thereby,  
memory 200 contains the running data of 261 most recently produced word  
30 groups, POINTER ( $n + 20$ ) through word group POINTER ( $n - 240$ ), associated with  
VOB ( $n + 20$ ) through VOB ( $n - 240$ ), respectively. The data of word group  
POINTER ( $n + 20$ ) are the most recently stored in memory 200. Whereas, the

data of word group POINTER (n-240) are the earliest stored and the next to be eliminated from memory 200.

As indicated before, the bit count or data space in output signal 106a of memory 106 that corresponds to VOB (n) is the same as in the recorded data on the disc, not shown. However, the bits of field VOB\_SRI 201a', for example, of FIGURE 2 of VOB (n) contain "dummy" data in signal 106a.

In carrying out an inventive feature, the aforementioned "dummy" data of field VOB\_SRI 201a' of FIGURE 2 of VOB (n) of signal 106a are substituted prior to the recording. The information contained in word START - ADDRESS (n+20) of word group POINTER (n+20) is used for generating word FWDI(m=20) of VOB(n) of FIGURE 3, to provide an address pointer to the 21<sup>th</sup> future VOB. The 21<sup>th</sup> future VOB, VOB (n+20), is located 20 VOB's behind current VOB (n) in the presentation sequence. Similarly, the information contained in each of words START - ADDRESS (n+20) through START - ADDRESS (n+1), START - ADDRESS (n-1) through START - ADDRESS (n-15), START - ADDRESS (n-20),

START - ADDRESS (n-60), START - ADDRESS (n-120) and START - ADDRESS (n-240) is used for generating words FWDI(m=20) FWDI(m=15) through FWDI (m=1), BWDI (m=1) through BWDI (m=15), BWDI (m=20), BWDI (m=60), BWDI (m=120) and BWDI (m=240), respectively. Accordingly, words FWDI (m=20). FWDI (m=15) through FWDI (m=+1), BWDI (m=1) through BWDI (m=15), BWDI (m=20), BWDI (m=60), BWDI (m=120) and BWDI (m=240) are produced in an output signal 200a of memory 200.

The bits of a data field VOB\_1STREF-EA, a data field VOB\_2NDREF-EA, and a data field VOB\_3RDREF-EA, not shown, of Nav-Pack 201, are required, in accordance with the Specification, to contain the last sector address of first reference, I -type picture frame, second reference, I or P-type picture frame and third reference, I or P-type picture frame, respectively. Those three data fields similarly contain "dummy" data in NAV\_PAK 201'. Accordingly, words FIRST-REFERENCE-ADDRESS (n), SECOND-REFERENCE—ADDRESS (n) and THIRD-REFERENCE—ADDRESS (n) of word group POINTER (n) are used for

producing, in signal 200a, the correct data bits of data field VOB<sub>U</sub>\_1STREF-EA, of data field VOB<sub>U</sub>\_2NDREF-EA and of data field VOB<sub>U</sub>\_3RDTREF-EA, respectively, not shown, of VOB<sub>U</sub>(n), in substitution of the aforementioned "dummy" data bits.

5           Signal 200a is coupled to data recorder 120 and recorded in data recorder 120 via a signal path formed by switch 107, when a second input terminal 107b of switch 107 is coupled to output terminal 107d. The coupling between terminals 107b and 107d is controlled by microprocessor 110 via control terminal 107c and occurs at the proper timings for substituting the  
10           corresponding "dummy" data bits of field VOB<sub>U</sub>\_SRI 201a' of FIGURE 2 of VOB<sub>U</sub> (n) with the correct data bits obtained from signal 200a.

          In carrying out an inventive feature, the pointer for each of the 60<sup>th</sup>, 120<sup>th</sup> and 240<sup>th</sup> future VOB<sub>U</sub>'s, ahead of current VOB<sub>U</sub> (n) of FIGURE 1, that would contain words FWDI(m = 60), FWDI(m = 120) and FWDI(m = 240), respectively, in  
15           accordance with the Specification, does not conform to the Specification. Whereas, the corresponding address pointers contained in words FWDI(m = 1)-FWDI(m = 20) are made to conform to the Specification. Consequently, the maximum fast forward trick mode occurs when word FWDI(m = 20) is selected for controlling the trick mode. Advantageously, such arrangement reduces the  
20           memory size requirement of memory 106 significantly, to the order of 8 Mega-Byte.

          In carrying out another inventive feature, in each of words FWDI(m = 60), FWDI(m = 120) and FWDI(m = 240), the pointer of, for example, the 20<sup>th</sup> future VOB<sub>U</sub>, contained in word FWDI(m = 20), is recorded. Thereby, any DVD player  
25           having the capability of operating in a fast forward trick mode, faster than that determined by word FWDI (m = 20) will be limited to that determined by word FWDI (m = 20).

          Packetizer/multiplexer 105 assigns each VOB<sub>U</sub> to a corresponding Cell such as Cell 206 of FIGURE 1 by establishing the proper values of corresponding  
30           data fields, not shown, in, for example, Nav\_Pack 201' of VOB<sub>U</sub> (n). The Specification, Section 4.5.4, subsection(s) 2 and 4, states that if a given word FWDI (or BWDI) were to point to an address of a VOB<sub>U</sub> outside the range of a

Cell, then such word FWDI (or BWDI) should contain a bit combination referred to as "does not exist" code, rather than an address of an actual VOB address.

In accordance with a further inventive feature, the size of each Cell is constrained by packetizer/multiplexer 105 to, for example, only 59 VOB's.

- 5 Therefore, the entries for each of words FWDI( $m = 60$ ), FWDI( $m = 120$ ) and FWDI( $m = 240$ ) need only contain the aforementioned "does not exist" code to conform to the Specification. Consequently, Nav\_Pack 201, for example, is made to conform to the Specification. The ability to limit the Cell size, such as of Cell 206, to only 59 VOB's is, advantageously, used to diminish the need for
- 10 a large buffer memory without any deviation from the Specification.

- The maximum number of Cells which can be included in a contiguous fashion so as to be presented seamlessly is 255. The presentation time of a VOB is between .4 and 1.2 seconds and nominally .5 seconds. The number of VOB's that would form a Cell is 59. Thus, a nominal recording of recording
- 15 content of 127.5 minutes is guaranteed to be seamless.

13  
CLAIMS

1. A data recorder apparatus for recording in a recording medium navigation data, facilitating a trick, playback mode of operation, and information data contained in each data unit of a sequence of data units forming an input data signal, comprising:

a buffer memory responsive to said input data signal for storing the information data of a running plurality of said data units and for reading out the stored information data of said running plurality of data units, sequentially, such that the information data of a data unit next-in-line to be recorded are developed at an output of said buffer memory;

means responsive to said input data signal for producing a value conforming to a position of a first address pointer of a plurality of address pointers of the navigation data of said next-in-line data unit, when the information data of a data unit associated with said first address pointer position and referred to by said first address pointer value are present concurrently with the information data of said next-in-line data unit in said buffer memory, said conforming value identifying where in said recording medium the data information of said associated data unit are designated to be recorded, the production of a value conforming to a position of a second address pointer of said plurality of address pointers of said next-in-line data unit being avoided, when the data information of a data unit associated with said second address pointer position are not present concurrently with the information data of said next-in-line data unit in said buffer memory; and

means for recording the information data and said navigation data of said given next-in-line data unit in said recording medium.

2. A data recorder apparatus according to Claim 1, wherein the data units recorded in said recording medium are compatible with a player conforming to Digital Versatile Disc (DVD) Specification for Read Only Disc.

14

3. A data recorder apparatus according to Claim 1, wherein said data unit forms a Video Object Unit (VOBU) and said plurality of address pointers are included in a field VOB Search Information (VOB-SRI) defined in the Digital Versatile Disc (DVD) Specification for Read Only Disc.

5

4. A data recorder apparatus according to Claim 1, further comprising means for producing the value of said second address pointer equal to said conforming value of said first address pointer.

10

5. A data recorder apparatus according to Claim 4, wherein said value of said second address pointer is made equal to a conforming value of an address pointer selected from said plurality of address pointers that identifies an associated data unit separated from said next-in-line data unit by a largest number of intervening data units.

15

6. A data recorder apparatus according to Claim 1 wherein said buffer memory comprises a first-in, first-out memory.

7. A data recorder apparatus for recording in a recording medium navigation data, facilitating a trick, playback mode of operation, and information data contained in each data unit of a sequence of data units partitioned into cells and forming an input data signal, comprising:

5 a buffer memory responsive to said input data signal for storing the information data of a running plurality of said data units and for reading out the stored information data of said running plurality of data units, sequentially, such that the information data of a data unit next-in-line to be recorded are developed at an output of said buffer memory;

10 means responsive to said input data signal for producing a value conforming to a position of a first address pointer of a plurality of address pointers of the navigation data of said next-in-line data unit, when a data unit associated with said first address pointer position is included with said next-in-line data unit in a common cell, said conforming value identifying

15 where in said recording medium the data information of said associated data unit are to be recorded, the production of a value conforming to a position of a second address pointer of said plurality of address pointers being avoided, when a data unit associated with said second address pointer and said next-in-line data unit are included in different cells;

20 means for limiting a number of data units that are included in said common cell to a limit value determined by a maximum number of

data units having information data that can be contained concurrently in said buffer memory; and

25 means for recording the information data and said navigation data of said next-in-line data unit in said recording medium.



1/4

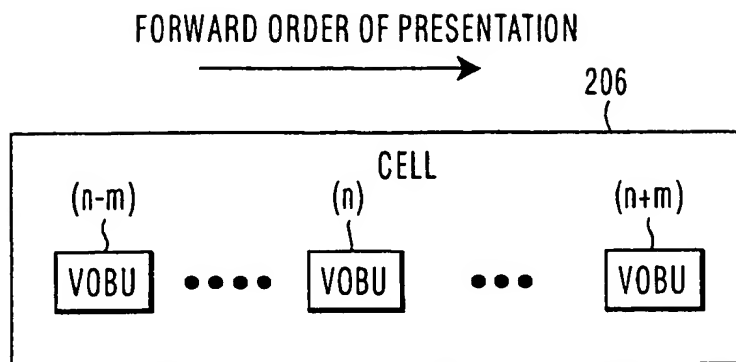


FIG. 1

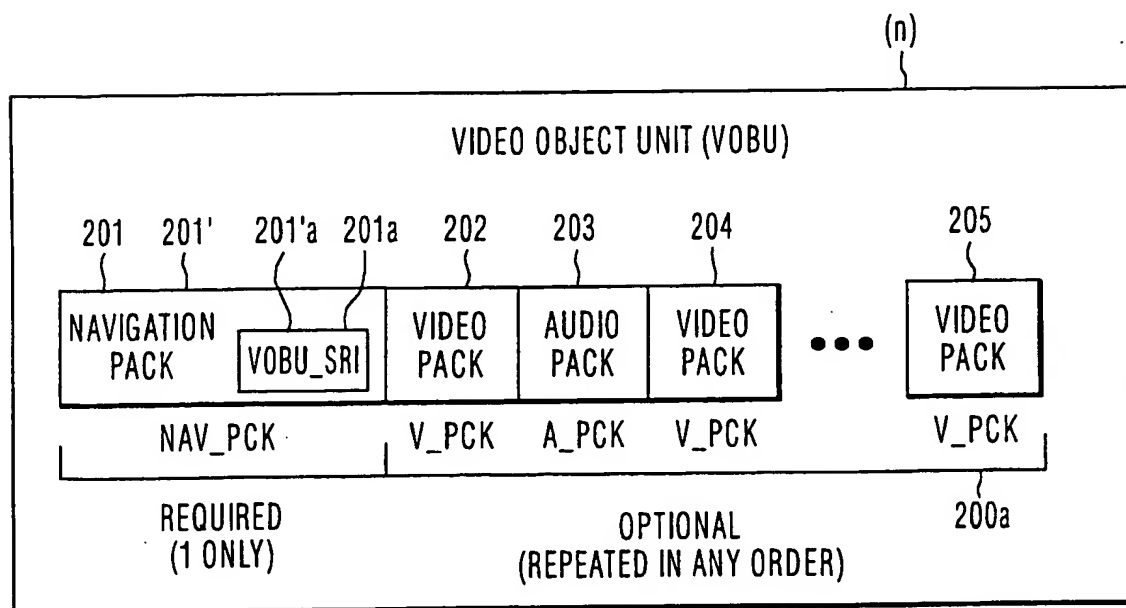


FIG. 2

2/4

FWDI (240)	+240 VOBUs start address
FWDI (120)	+120 VOBUs start address
FWDI (60)	+ 60 VOBUs start address
FWDI (20)	+ 20 VOBUs start address
FWDI (15)	+ 15 VOBUs start address
FWDI (14)	+ 14 VOBUs start address
FWDI (13)	+ 13 VOBUs start address
FWDI (12)	+ 12 VOBUs start address
FWDI (11)	+ 11 VOBUs start address
FWDI (10)	+ 10 VOBUs start address
FWDI (9)	+ 9 VOBUs start address
FWDI (8)	+ 8 VOBUs start address
FWDI (7)	+ 7 VOBUs start address
FWDI (6)	+ 6 VOBUs start address
FWDI (5)	+ 5 VOBUs start address
FWDI (4)	+ 4 VOBUs start address
FWDI (3)	+ 3 VOBUs start address
FWDI (2)	+ 2 VOBUs start address
FWDI (1)	+ 1 VOBUs start address

FIG. 3A

3/4

BWDI (1)	- 1 VOBUs start address
BWDI (2)	- 2 VOBUs start address
BWDI (3)	- 3 VOBUs start address
BWDI (4)	- 4 VOBUs start address
BWDI (5)	- 5 VOBUs start address
BWDI (6)	- 6 VOBUs start address
BWDI (7)	- 7 VOBUs start address
BWDI (8)	- 8 VOBUs start address
BWDI (9)	- 9 VOBUs start address
BWDI (10)	- 10 VOBUs start address
BWDI (11)	- 11 VOBUs start address
BWDI (12)	- 12 VOBUs start address
BWDI (13)	- 13 VOBUs start address
BWDI (14)	- 14 VOBUs start address
BWDI (15)	- 15 VOBUs start address
BWDI (20)	- 20 VOBUs start address
BWDI (60)	- 60 VOBUs start address
BWDI (120)	-120 VOBUs start address
BWDI (240)	-240 VOBUs start address

FIG. 3B

4/4

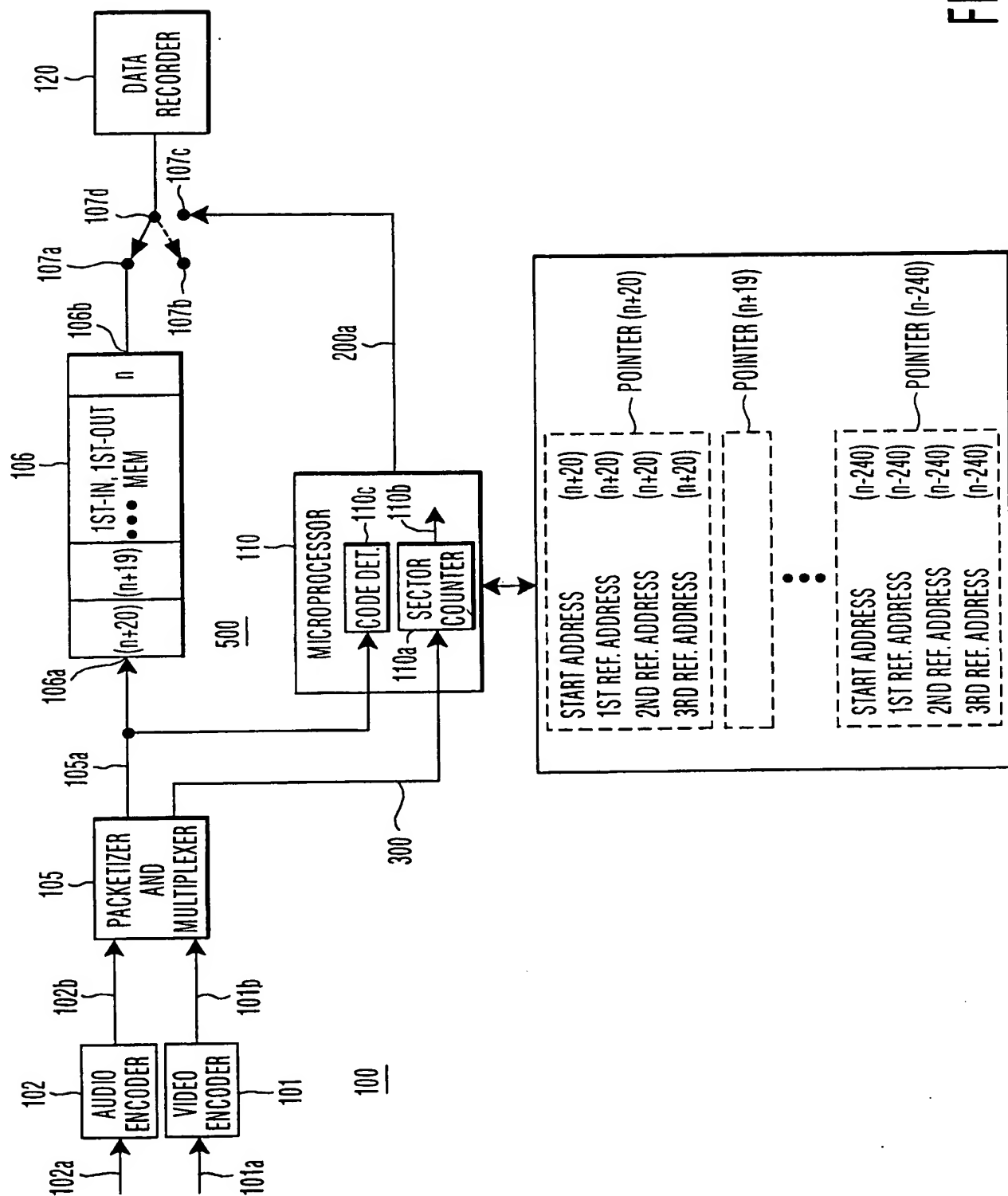


FIG. 4

# INTERNATIONAL SEARCH REPORT

In. ational Application No

PCT/US 00/30344

**A. CLASSIFICATION OF SUBJECT MATTER**  
IPC 7 H04N5/783

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 817 480 A (MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.) 7 January 1998 (1998-01-07) page 6, line 15 -page 12, line 43; figures 2-7	1-3,7
A	EP 0 724 264 A (KABUSHIKI KAISHA TOSHIBA) 31 July 1996 (1996-07-31) column 12, line 47 -column 14, line 28 column 21, line 3 - line 13 column 23, line 13 -column 26, line 11 column 33, line 26 -column 36, line 59 column 38, line 3 -column 41, line 34; figures 6,31-35A,42A-43,45-51	1-3,7

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

\* Special categories of cited documents :

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
- \*E\* earlier document but published on or after the international filing date
- \*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*O\* document referring to an oral disclosure, use, exhibition or other means
- \*P\* document published prior to the international filing date but later than the priority date claimed

\*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

- \*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- \*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- \*G\* document member of the same patent family

Date of the actual completion of the international search

2 March 2001

Date of mailing of the international search report

09/03/2001

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Verleye, J

# INTERNATIONAL SEARCH REPORT

In International Application No

PCT/US 00/30344

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>EP 0 795 871 A (PIONEER ELECTRONIC CORPORATION)  17 September 1997 (1997-09-17)  page 6, line 33 -page 7, line 46  page 10, line 13 - line 39  page 13, line 52 -page 15, line 9; figures 1,5A,10</p> <p style="text-align: center;">-----</p>	1-3,7

# INTERNATIONAL SEARCH REPORT

Information on patent family members

In International Application No

PCT/US 00/30344

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
EP 817480	A	07-01-1998	DE 69603587 D	09-09-1999
			DE 69603587 T	30-03-2000
			JP 2883209 B	19-04-1999
			CN 1181859 A	13-05-1998
			HK 1005972 A	07-04-2000
			WO 9629822 A	26-09-1996
			US 5838872 A	17-11-1998
<hr/>				
EP 724264	A	31-07-1996	AT 174149 T	15-12-1998
			AT 191292 T	15-04-2000
			CA 2168327 A	31-07-1996
			CN 1134583 A	30-10-1996
			DE 69601039 D	14-01-1999
			DE 69601039 T	24-06-1999
			DE 69607489 D	04-05-2000
			DE 69607489 T	21-09-2000
			EP 0872839 A	21-10-1998
			JP 2747268 B	06-05-1998
			JP 8273304 A	18-10-1996
			JP 2857126 B	10-02-1999
			JP 10074379 A	17-03-1998
			JP 2875237 B	31-03-1999
			JP 10074380 A	17-03-1998
			KR 209846 B	15-07-1999
			KR 209851 B	15-07-1999
			KR 220529 B	15-09-1999
			US 6118927 A	12-09-2000
			US 5870523 A	09-02-1999
<hr/>				
EP 795871	A	17-09-1997	JP 9252450 A	22-09-1997
			CN 1170930 A	21-01-1998
			US 6148138 A	14-11-2000
<hr/>				